

FIGURE 1

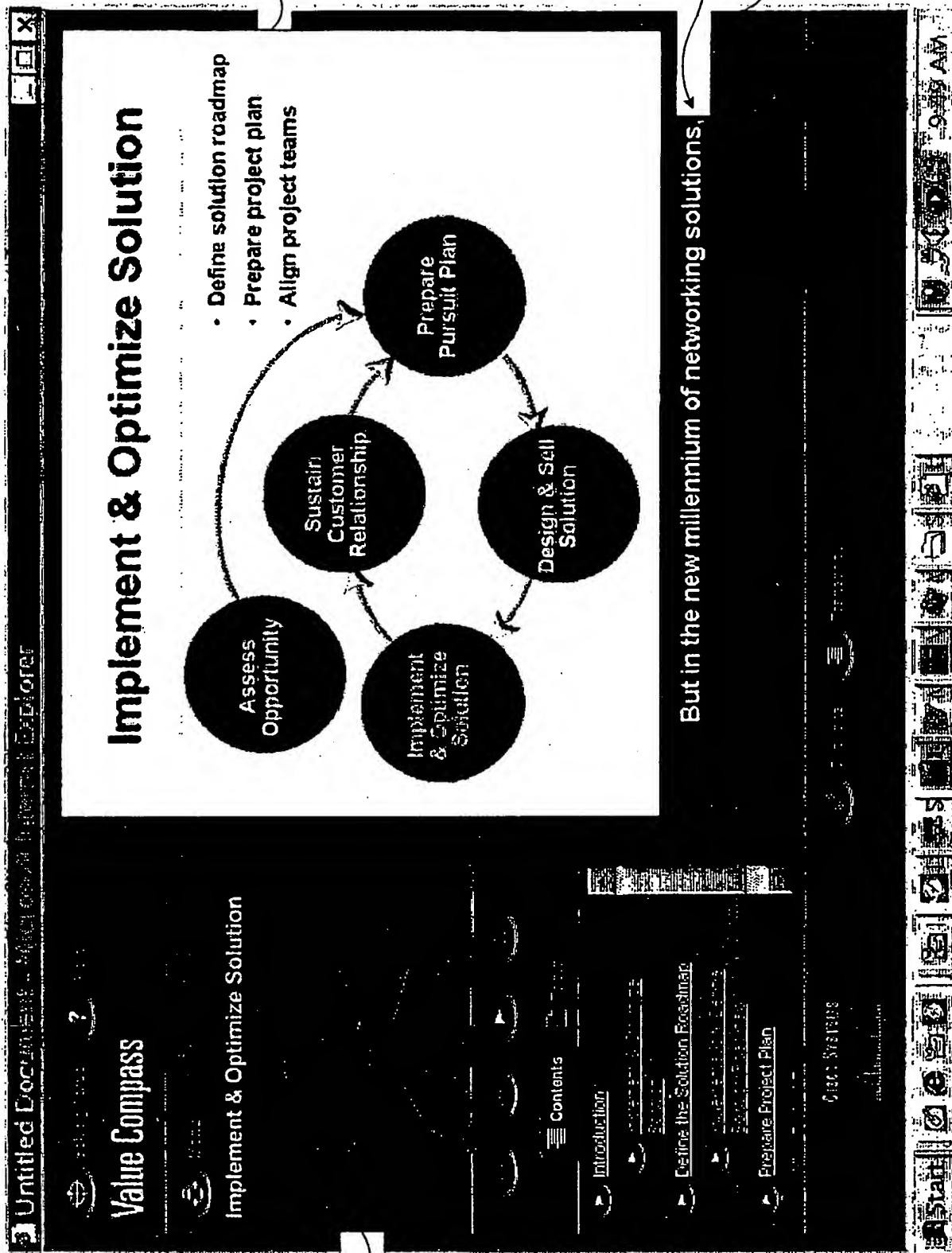
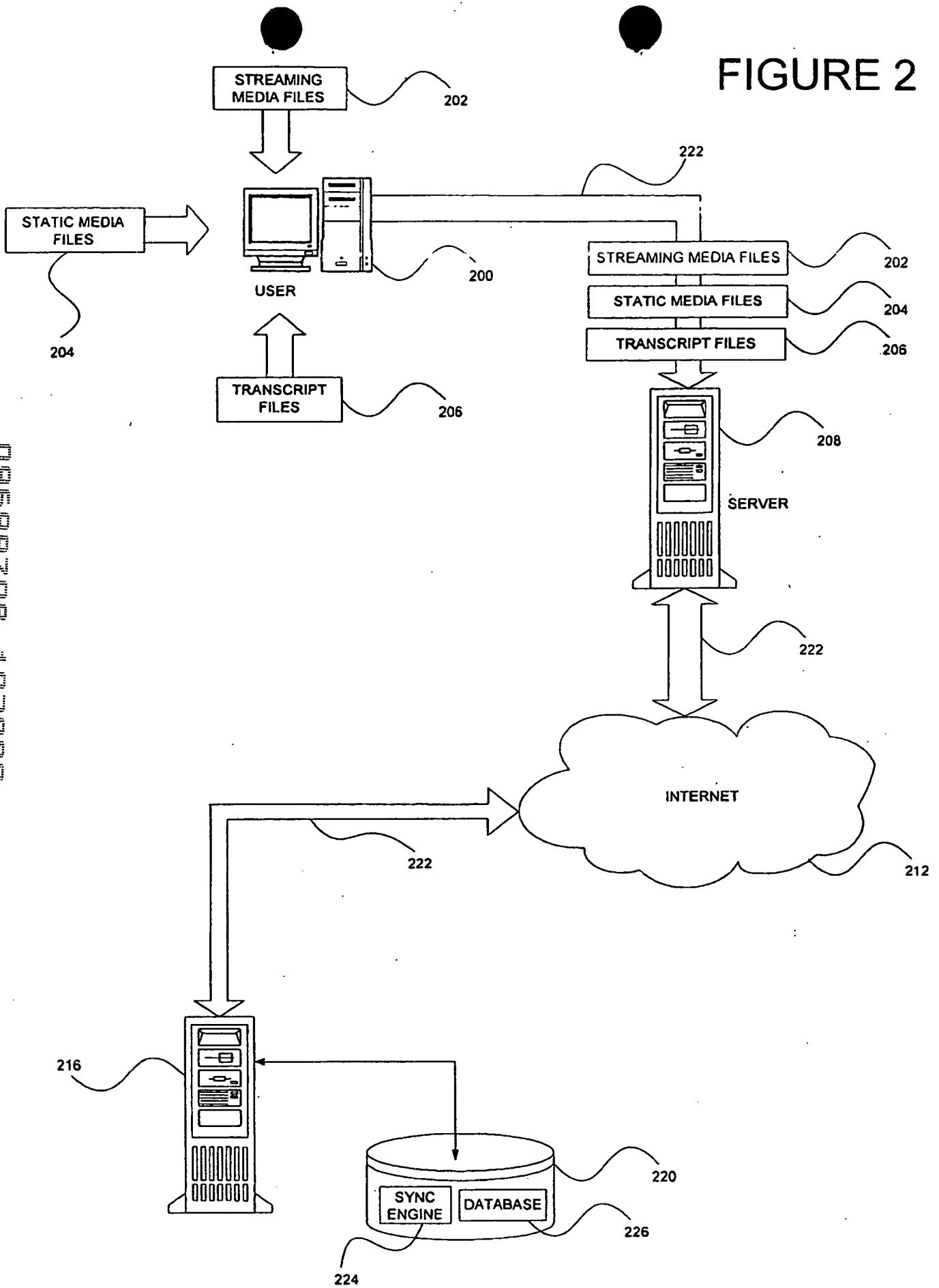
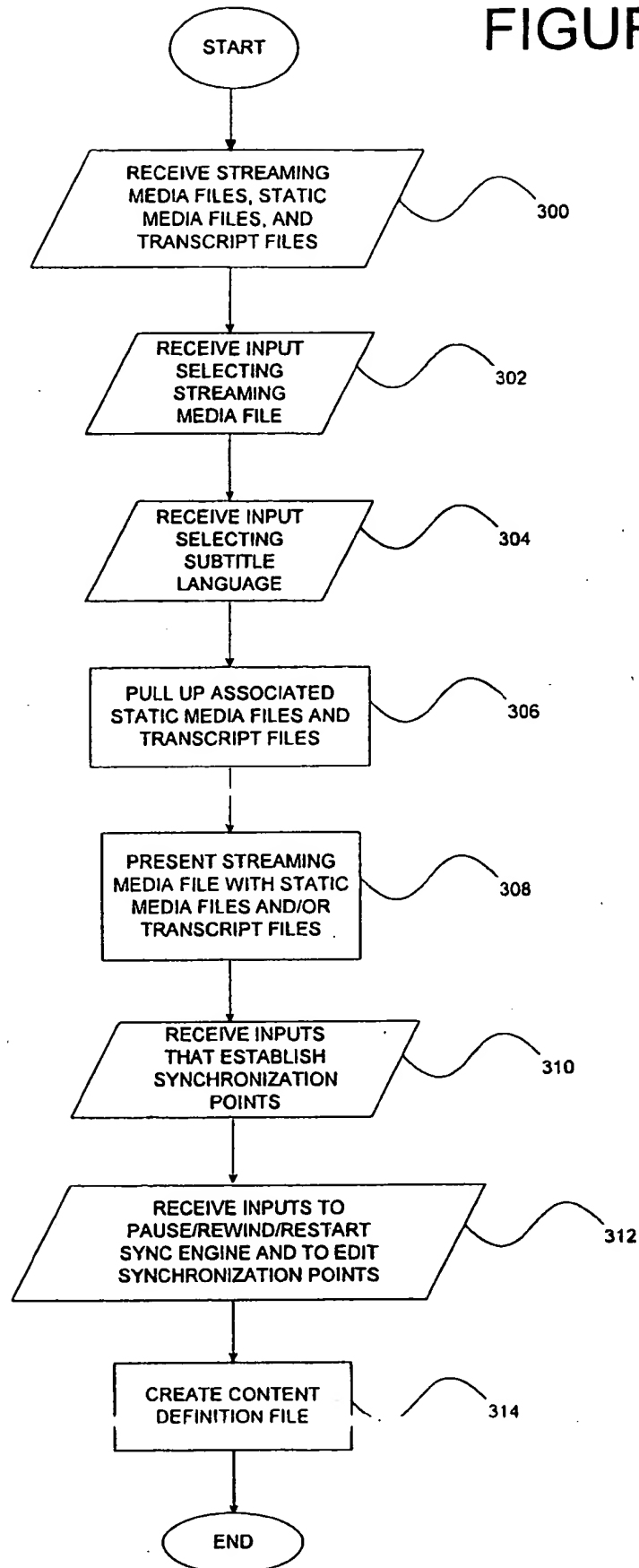


FIGURE 2

000001-86256960





DOCKET# 362659 FIGURE 4

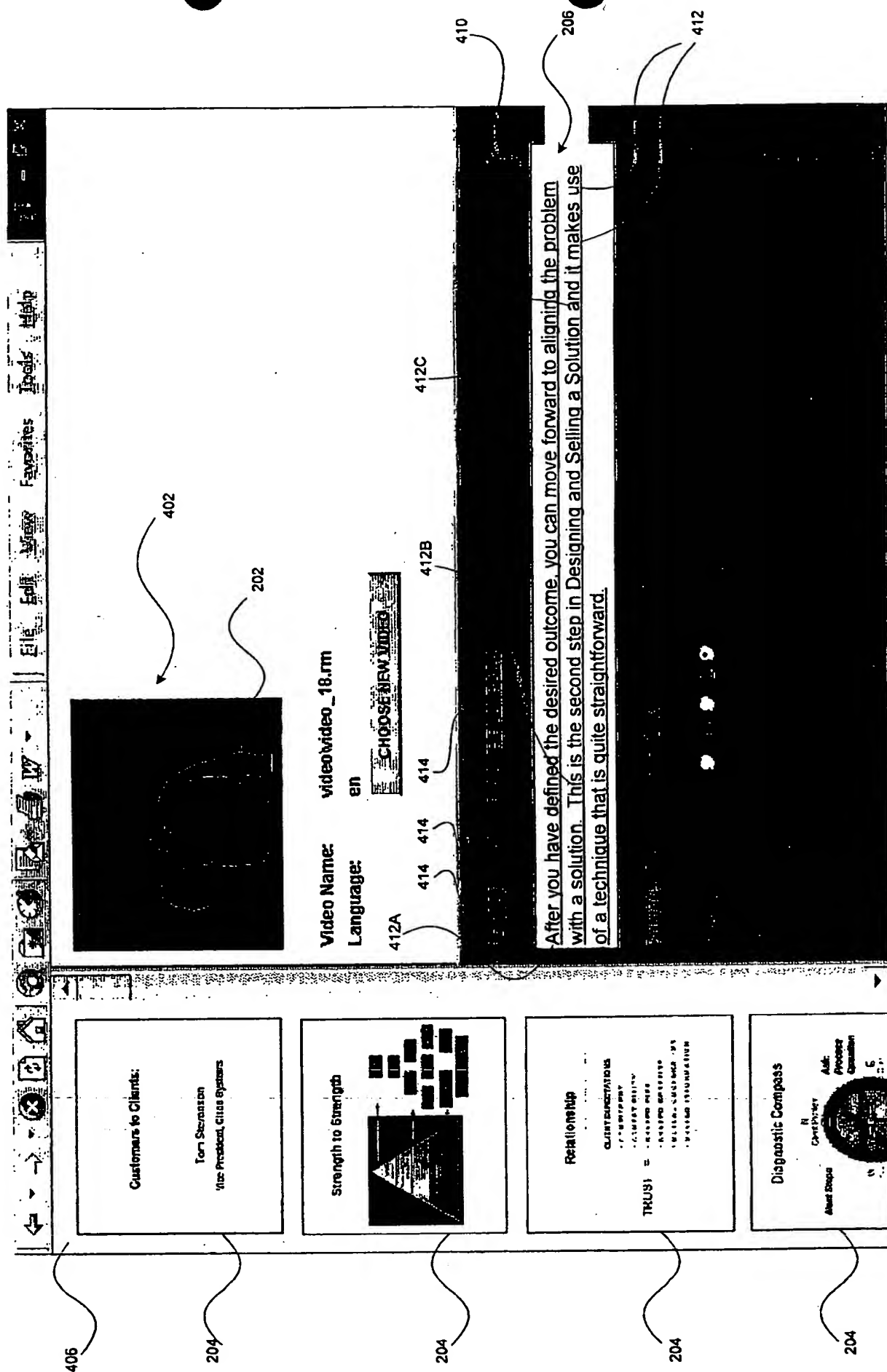
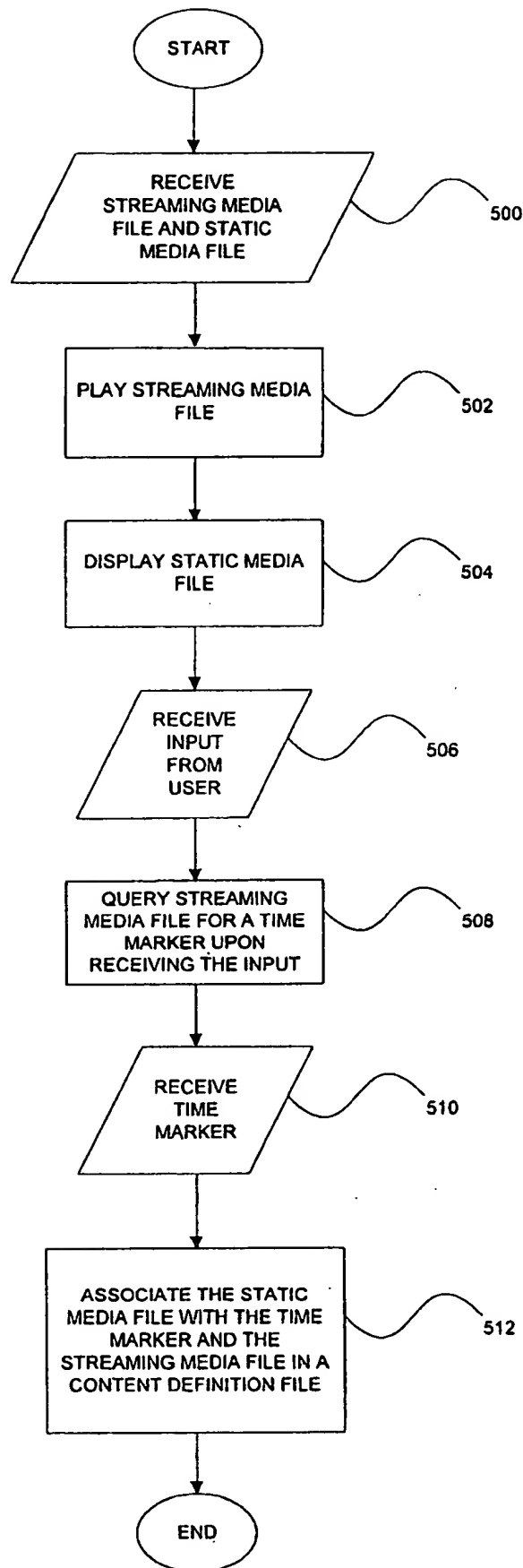


FIGURE 5A



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FIGURE 6A

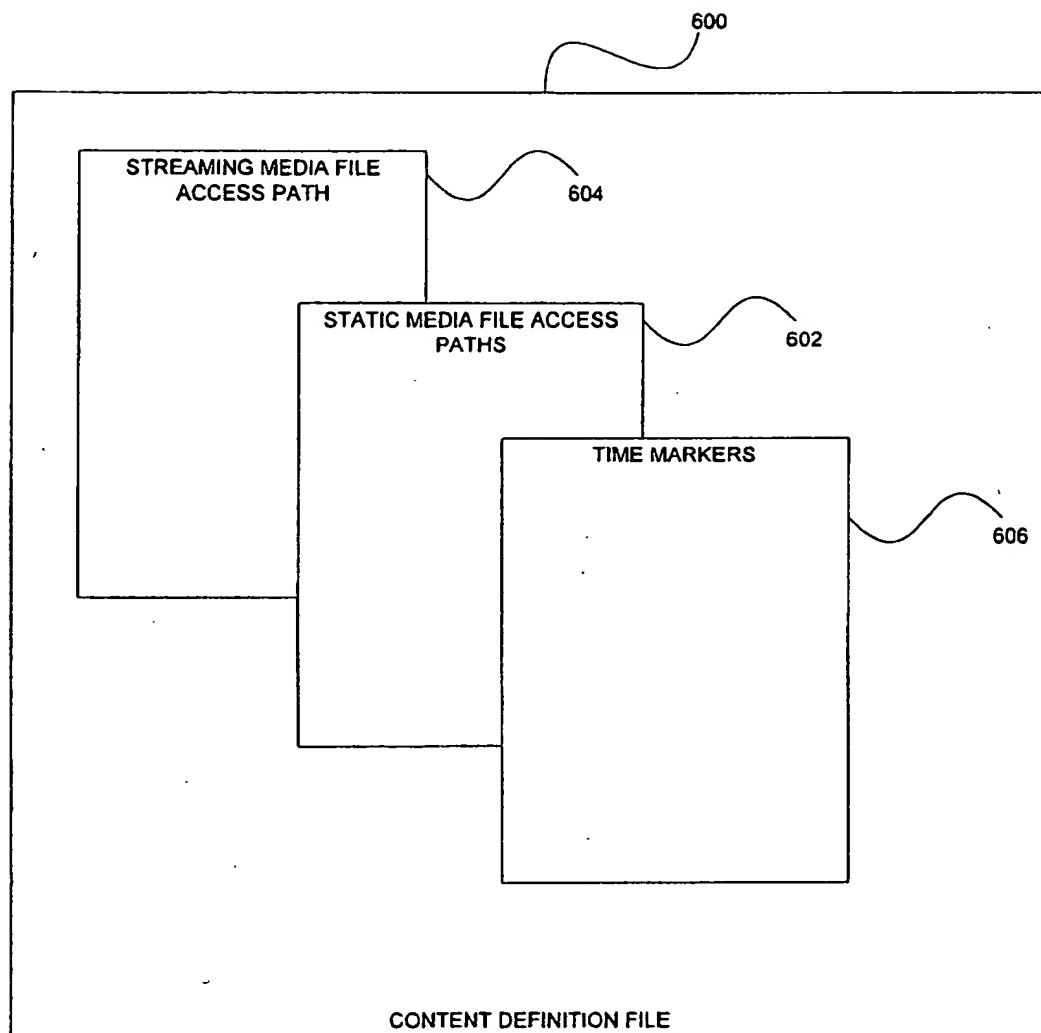
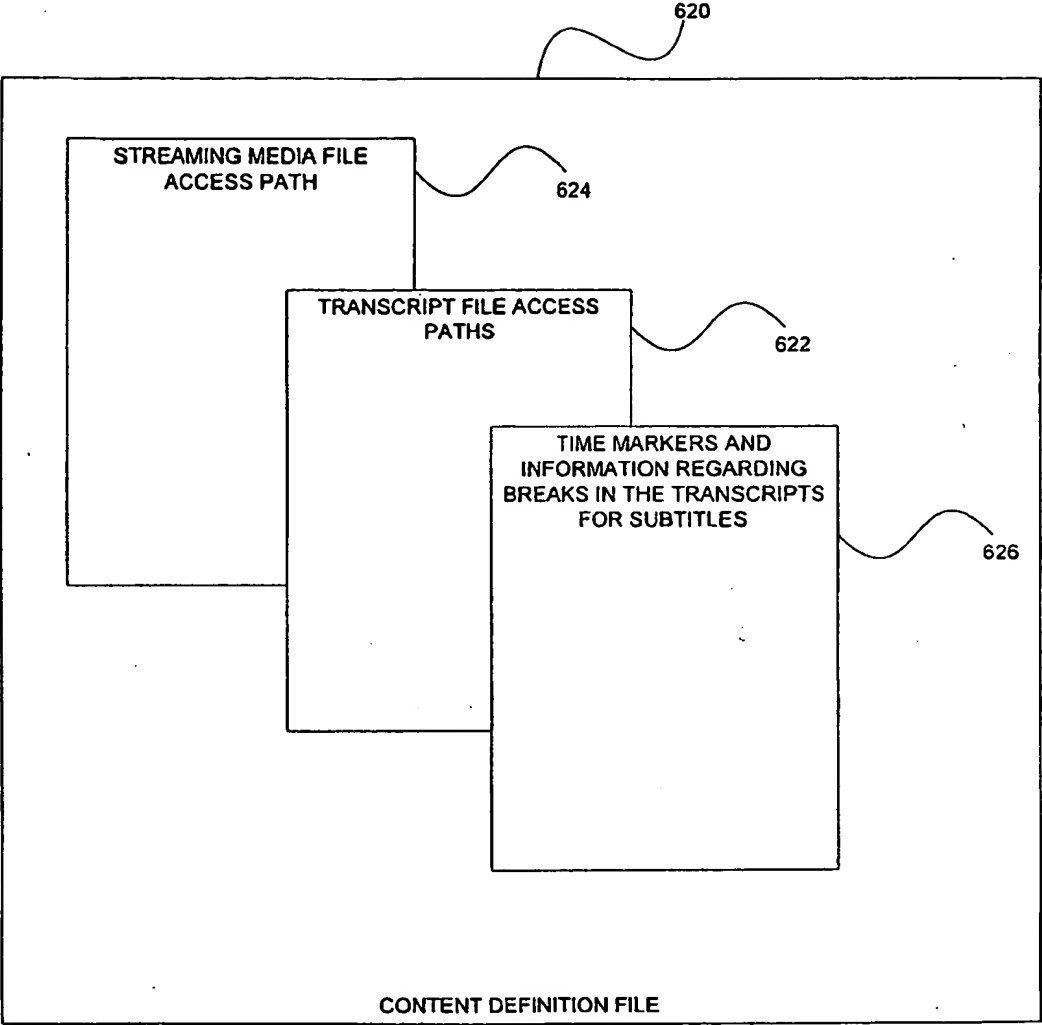


FIGURE 6B



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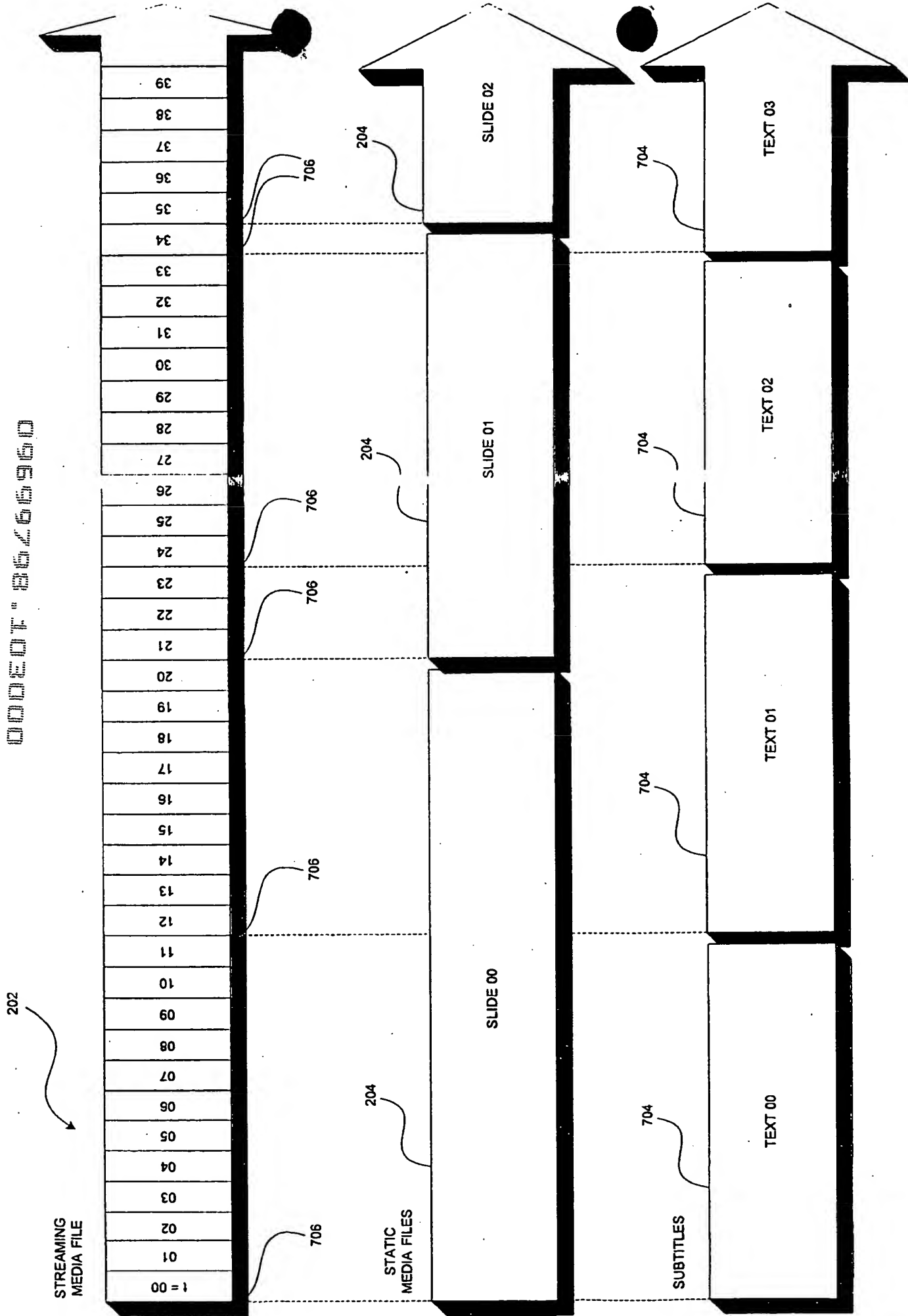


FIGURE 7

000001" 86/06960

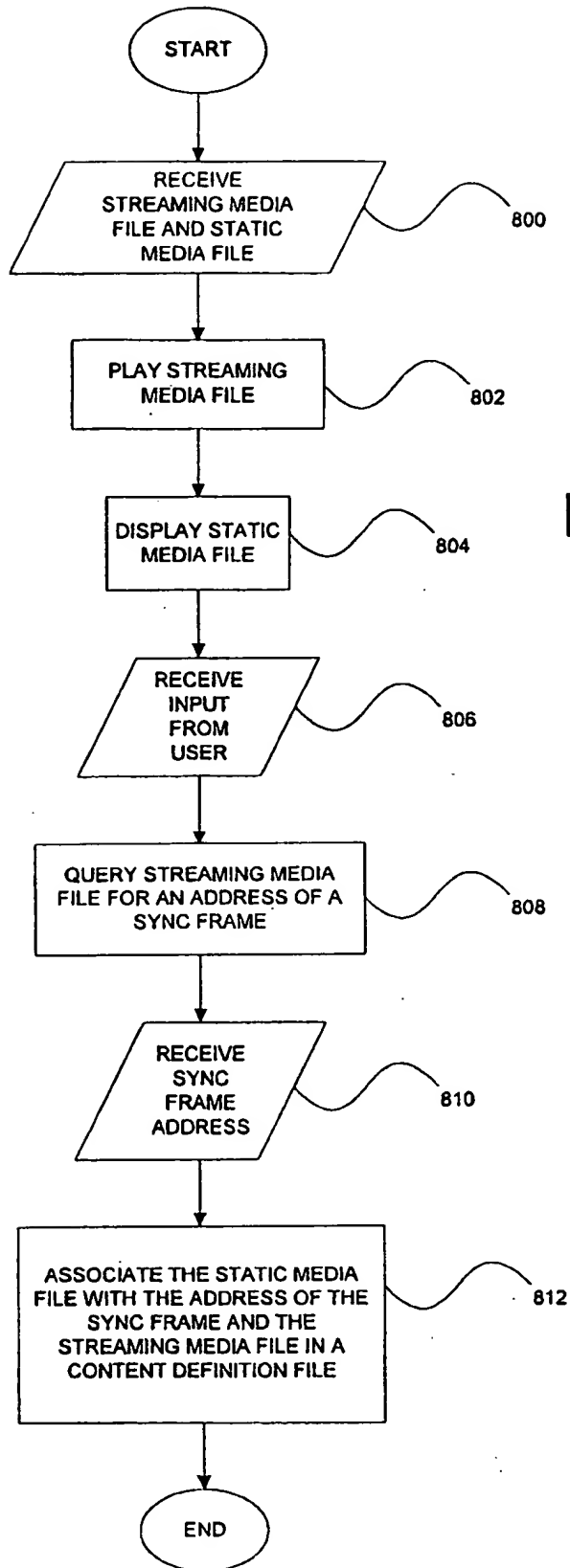


FIGURE 8

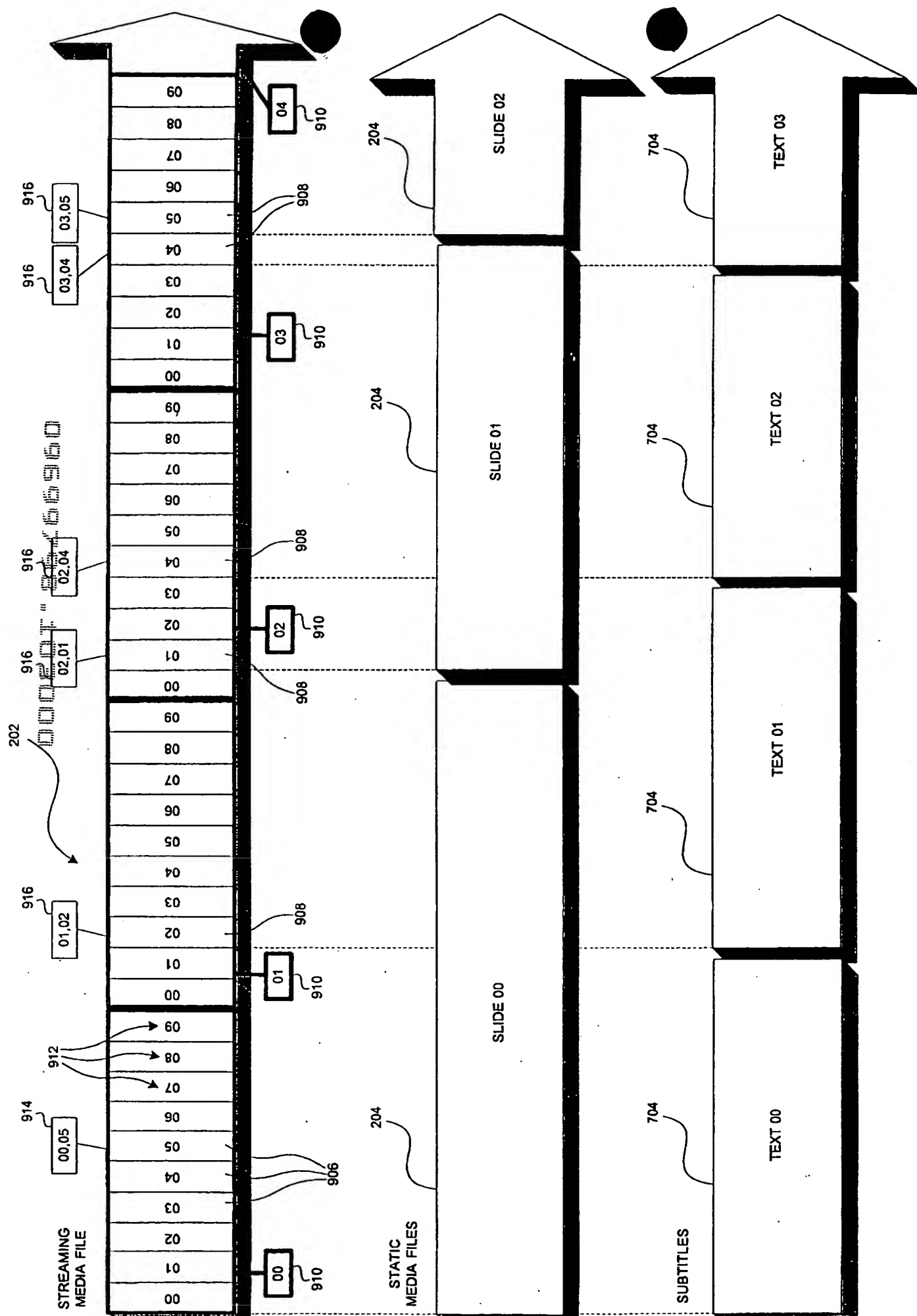
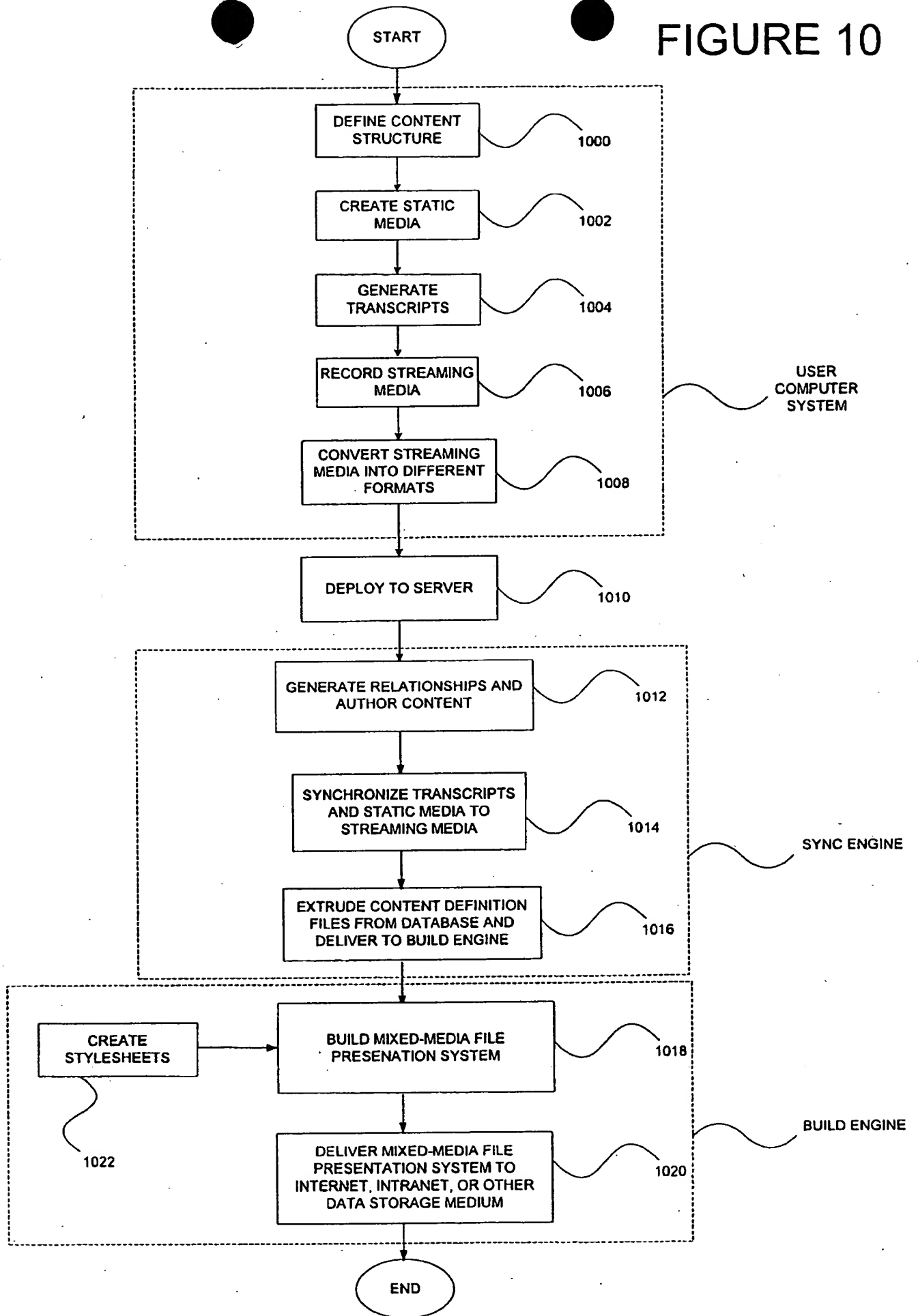


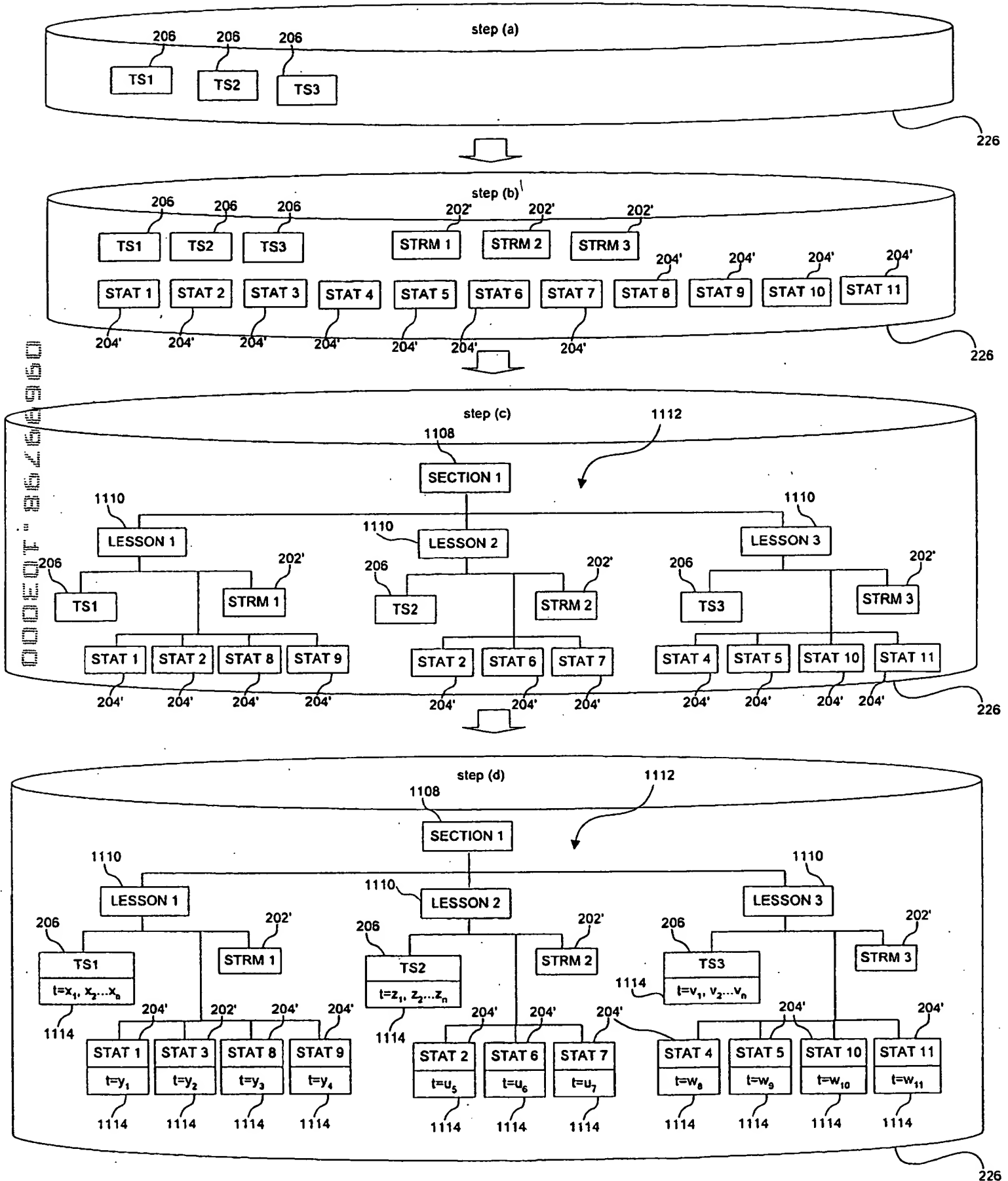
FIGURE 9

FIGURE 10



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FIGURE 11



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graph TD
    subgraph 1200 [Computer System 1200]
        P[PROCESSOR 1204] <--> B[BUS 1202]
        MM[MAIN MEMORY 1206] <--> B
        R[ROM 1208] <--> B
        C[COMMUNICATION INTERFACE 1218] <--> B
        S[STORAGE DEVICE 1210] <--> B
    end

    D[DISPLAY 1212] <--> B
    I[INPUT DEVICE 1214] <--> B
    CC[CURSOR CONTROL 1216] <--> B

    S1[SERVER 1230] --- I1((INTERNET/ISP 1226))
    I1 --- I2((LOCAL NETWORK 1222))
    I2 --- H[HOST 1224]
    I2 --- N[NETWORK LINK 1220]
    N --- 1200
  
```

FIGURE 12